

ES_LPC84x

Errata sheet LPC84x

Rev. 1.1 — 2 March 2018

Errata sheet

Document information

Info	Content
Keywords	LPC845M301JBD64;LPC845M301JBD48;LPC845M301JHI48; LPC845M301JHI33;LPC844M201JBD64;LPC844M201JBD48; LPC844M201JHI48;LPC844M201JHI33; LPC84x errata
Abstract	This errata sheet describes both the known functional problems and any deviations from the electrical specifications known at the release date of this document. Each deviation is assigned a number and its history is tracked in a table.



Revision history

Rev	Date	Description
1.1	20180302	<ul style="list-style-type: none">• Added ROM.1
1.0	20170614	Initial version

Contact information

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1. Product identification

The LPC84x LQFP64 and LQFP48 packages have the following top-side marking:

- First line: LPC84xMy01
 - y: 3 or 2
- Second line: xxxxxx
- Third line: xxxyywwx[R]x
 - yyww: Date code with yy = year and ww = week.
 - xR = Boot code version and device revision.

The LPC84x HVQFN48 and HVQFN33 packages have the following top-side marking:

- First line: LPC84xMy01
 - y: 3 or 2
- Second line: xxxxxx
- Third line: xxxyywwx[R]x
 - yyww: Date code with yy = year and ww = week.
 - xR = Boot code version and device revision.

Table 1. Device revision table

Revision identifier (R)	Revision description
1A	Initial device revision with Boot ROM version 13.1

2. Errata overview

Table 2. Functional problems table

Functional problems	Short description	Revision identifier	Detailed description
ROM.1	The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command) is not functional.	'1A'	Section 3.1

Table 3. AC/DC deviations table

AC/DC deviations	Short description	Detailed description
n/a	n/a	n/a

Table 4. Errata notes

Note	Short description	Detailed description
n/a	n/a	n/a

3. Functional problems detail

3.1 ROM.1: The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command) is not functional.

Introduction:

On the LPC84x, In-System programming (ISP) calls are available for programming or reprogramming the on-chip flash memory, using the boot loader software and USART, I2C, or SPI serial port. The Boot image ISP call for I2C interface or SPI interface (I2C/SPI SH_CMD_BOOT, 0xA3) can be used to boot the application currently programmed into flash, boot address starting at 0x0.

Problem:

The Boot image ISP call for I2C interface or SPI interface (SH_CMD_BOOT command, 0xA3) is not functional.

Work-around:

Use the Reset device ISP call for I2C interface or SPI interface (SH_CMD_RESET command, 0xA2). This command can be used to reset the LPC84x device.

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