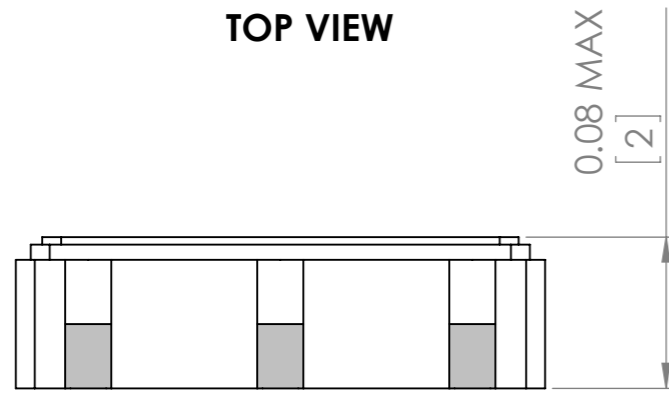
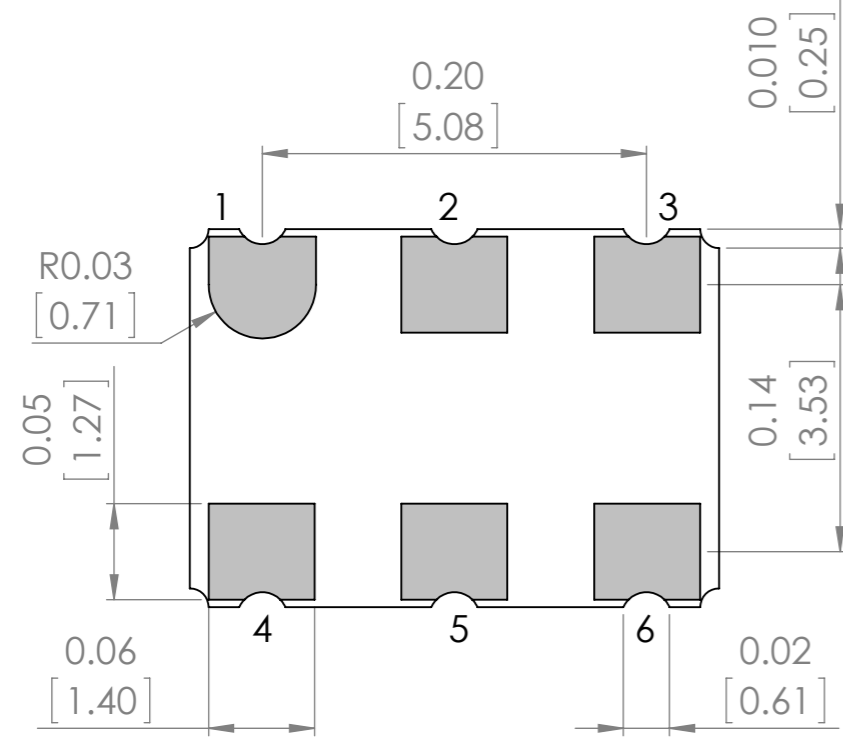


TOP VIEW



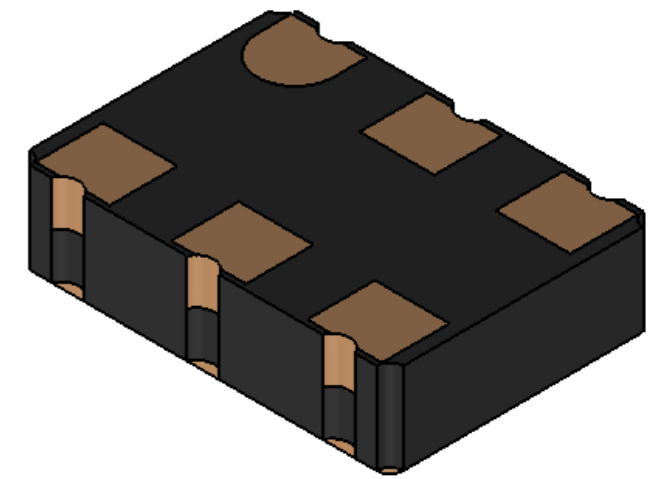
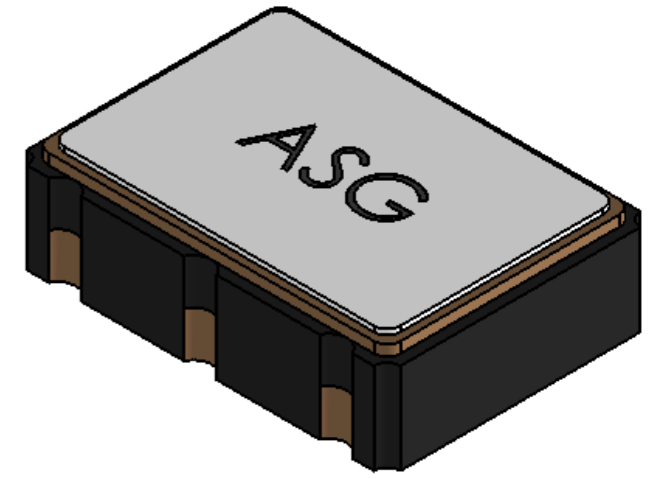
SIDE VIEW




BOTTOM VIEW

Pin #	Pin Description For VCXO configuration
1	VoltageControl for VCXO
2	Output Enable (OE)
3	GND
4	RF Output
5	RF Output
6	Vdd

Pin #	Pin Description For XO configuration
1	Output Enable (OE)
2	N/C for XO
3	GND
4	RF Output
5	RF Output
6	Vdd



UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCH(MM) SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:	DEBUR AND BREAK SHARP EDGES	DO NOT SCALE DRAWING	REVISION
DRAWN	NAME	SIGNATURE	DATE			 30332 Esperanza, Rancho Santa margarita, California 92688	
CHK'D	SAAZVAT					TITLE: MEMS	
APPV'D	XXXXXX					DWG NO. ASG-D	
MFG						A3	
Q.A				MATERIAL:		SCALE:10:1	
				WEIGHT:		SHEET 1 OF 1	