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## PIC16F72 Data Sheet Errata

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The PIC16F72 parts you have received conform functionally to the Device Data Sheet (DS39597B), except for the anomalies described below.

All of the issues listed here will be addressed in future revisions of the PIC16F72 silicon.

None.

### Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS39597B), the following clarifications and corrections should be noted.

#### 1. Module: Core (Product Identification System)

The information provided in the Product Identification System (page 133) implies that Extended Temperature range devices are not available for the PIC16F72. This is not the case. PIC16F72 devices, with the "E" suffix as part of their product identifiers, are capable of Extended Temperature operation (-40°C to +125°C).

The Product Identification System is amended by the addition of the following entry under "Temperature Range":

E = -40°C to +125°C

#### 2. Module: Core (Product Identification System)

The information provided in the Product Identification System (page 133) implies that PIC16F72 devices are available in Plastic DIP (PDIP) packages. This is not the case. This information has been removed and replaced with the correct package type of Skinny Plastic DIP (SP).

The Product Identification System is amended by the addition of the following entry under "Package":

SP = Skinny Plastic DIP

Example:  
PIC16F72-04/SP

# PIC16F72

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## 3. Module: PORTB Register Summary Table

In the Device Data Sheet, Table 3-4 shows bit 6 (INTEDG) of the OPTION register as shaded, or unused by PORTB. This is incorrect. The table has been updated as shown below:

**TABLE 3-4: SUMMARY OF REGISTERS ASSOCIATED WITH PORTB**

Address	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Value on POR, BOR	Value on all other Resets
06h, 106h	PORTB	RB7	RB6	RB5	RB4	RB3	RB2	RB1	RB0	xxxx xxxx	uuuu uuuu
86h, 186h	TRISB	PORTB Data Direction Register								1111 1111	1111 1111
81h, 181h	OPTION	RBPU	INTEDG	T0CS	T0SE	PSA	PS2	PS1	PS0	1111 1111	1111 1111

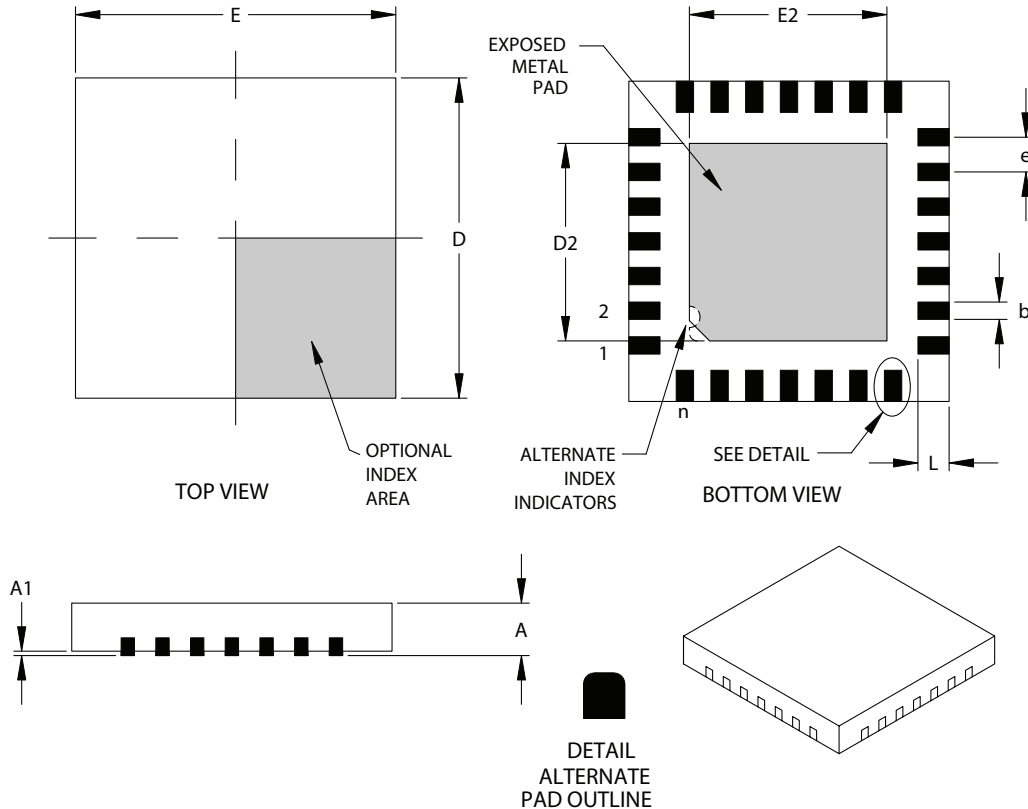
**Legend:** x = unknown, u = unchanged. Shaded cells are not used by PORTB.

## 4. Module: Package Marking Information

The package drawing on page 121 of the Device Data Sheet no longer applies; refer to the package information below.

The solder mask drawing for the 28-pin QFN package on page 122 of the Device Data Sheet has been omitted. This drawing is from older documentation and no longer applies.

### 28-Lead Plastic Quad Flat No Lead Package (ML) 6x6 mm Body (QFN) – With 0.55 mm Contact Length (Saw Singulated)



Dimension Limits		UNITS			MILLIMETERS*		
		MIN	NOM	MAX	MIN	NOM	MAX
Number of Pins	n	28			28		
Pitch	e	.026 BSC			0.65 BSC		
Overall Height	A	.031	.035	.039	0.80	0.90	1.00
Standoff	A1	.000	.001	.002	0.00	0.02	0.05
Contact Thickness	A3	.008 REF			0.20 REF		
Overall Width	E	.232	.236	.240	5.90	6.00	6.10
Exposed Pad Width	E2	.140	.146	.152	3.55	3.70	3.85
Overall Length	D	.232	.236	.240	5.90	6.00	6.10
Exposed Pad Length	D2	.140	.146	.152	3.55	3.70	3.85
Contact Width	b	.009	.011	.013	0.23	0.28	0.33
Contact Length	L	.018	.022	.024	0.45	0.55	0.65

\*Controlling Parameter

Notes:

JEDEC equivalent: MO-220

Drawing No. C04-105

Revised 02-18-05

# PIC16F72

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## REVISION HISTORY

### Rev A Document (3/2003)

First revision of this document. Data Sheet clarification issue 1 (Core – Product Identification System).

### Rev B Document (12/2004)

Added Data Sheet clarification issue 2 (Core – Product Identification System) and 3 (PORTB Register Summary Table).

### Rev C Document (8/2005)

Added Data Sheet clarification issue 4 (Package Marking Information).

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