

Product Summary

- Continuous Drain-Source Voltage: 60V
- On-State Resistance: 200mΩ
- Nominal Load Current ($V_{IN} = 5V$): 1.8A
- Clamping Energy: 120mJ

Description

The ZXMS6005DN8Q is a dual self-protected low side IntelliFET™ MOSFET with logic level input. It integrates overtemperature, overcurrent, overvoltage (active clamp) and ESD protected logic level functionality. The ZXMS6005DN8Q is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

Applications

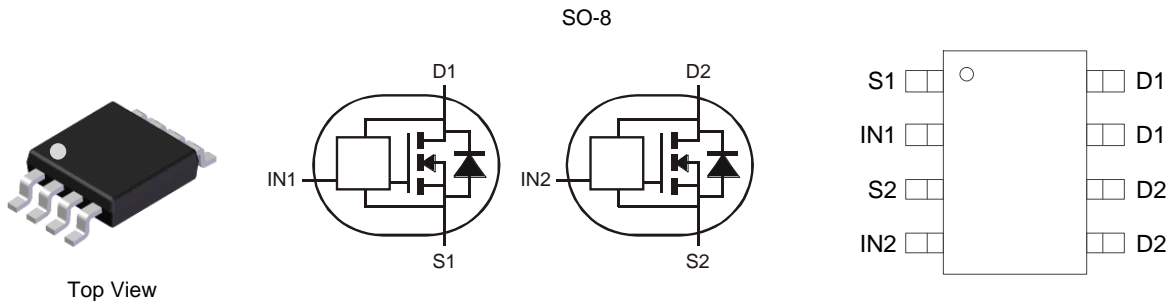
- Lamp Driver
- Motor Driver
- Relay Driver
- Solenoid Driver

Features and Benefits

- Low Input Current
- Logic Level Input (3.3V and 5V)
- Short Circuit Protection with Auto Restart
- Overvoltage Protection (Active Clamp)
- Thermal Shutdown with Auto Restart
- Overcurrent Protection
- Input Protection (ESD)
- High Continuous Current Rating
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish⁽³⁾
- Weight: 79.1mg (Approximate)

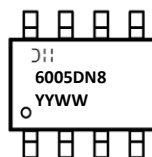


Ordering Information (Note 5)

| Part Number | Marking | Reel Size (inches) | Tape Width (mm) | Quantity per Reel |
|-----------------|---------|--------------------|-----------------|-------------------|
| ZXMS6005DN8Q-13 | 6005DN8 | 13 | 12 | 2,500 units |

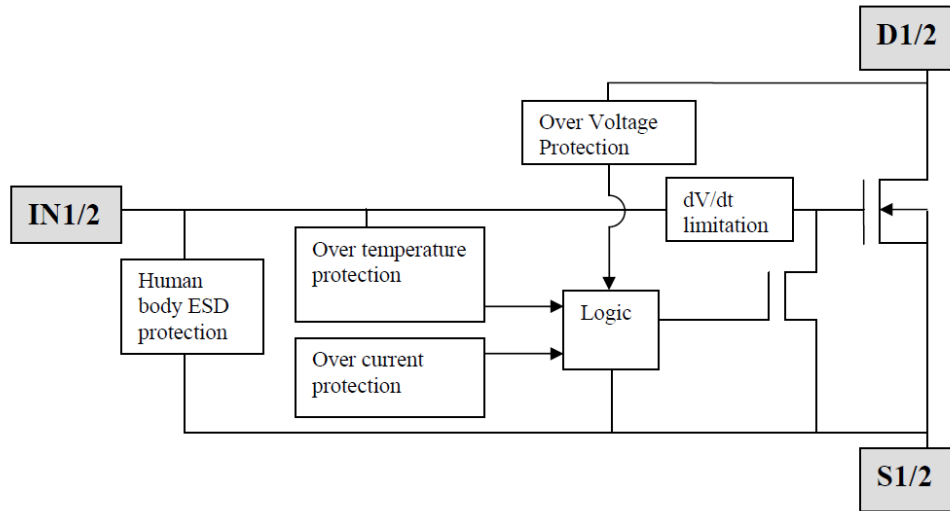
- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
 5. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



⌋⌋ = Manufacturer's Marking
 6005DN8 = Product Type Marking Code
 YYWW = Date Code Marking
 YY: Year
 WW: Week: 01~52;
 52 Represents 52 and 53 Week

Functional Block Diagram



Application Information

- Two Completely Isolated Independent Channels
- Especially Suited for Loads with a High In-rush Current Such as Lamps and Motors
- All Types of Resistive, Inductive and Capacitive Loads in Switching Applications
- μC Compatible Power Switch for 12V and 24V DC Applications
- Replaces Electromechanical Relays and Discrete Circuits
- Linear Mode Capability — the current-limiting protection circuitry is designed to deactivate at low V_{DS} to minimize on-state power dissipation. The maximum DC operating current is therefore determined by the thermal capability of the package or board combination, rather than by the protection circuitry. This does not compromise the product's ability to self-protect at low V_{DS} .

Absolute Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise stated.)

| Characteristic | Symbol | Value | Unit |
|---|---------------------|--------------------------|------|
| Continuous Drain-Source Voltage | V_{DS} | 60 | V |
| Drain-Source Voltage For Short Circuit Protection | $V_{\text{DS(SC)}}$ | 16 | V |
| Continuous Input Voltage | V_{IN} | -0.5 to +6 | V |
| Continuous Input Current @ $-0.2\text{V} \leq V_{\text{IN}} \leq 6\text{V}$ | I_{IN} | No Limit | mA |
| Continuous Input Current @ $V_{\text{IN}} < -0.2\text{V}$ or $V_{\text{IN}} > 6\text{V}$ | | $ I_{\text{IN}} \leq 2$ | |
| Pulsed Drain Current @ $V_{\text{IN}} = 3.3\text{V}$ | I_{DM} | 5 | A |
| Pulsed Drain Current @ $V_{\text{IN}} = 5\text{V}$ | I_{DM} | 6 | A |
| Continuous Source Current (Body Diode) (Note 6) | I_{S} | 2.5 | A |
| Pulsed Source Current (Body Diode) | I_{SM} | 10 | A |
| Unclamped Single Pulse Inductive Energy, $T_J = +25^\circ\text{C}$, $I_{\text{D}} = 0.5\text{A}$, $V_{\text{DD}} = 24\text{V}$ | E_{AS} | 120 | mJ |
| Electrostatic Discharge (Human Body Model) | V_{HBM} | 4,000 | V |
| Charged Device Model | V_{CDM} | 1,000 | V |

Note: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Recommended Operating Conditions

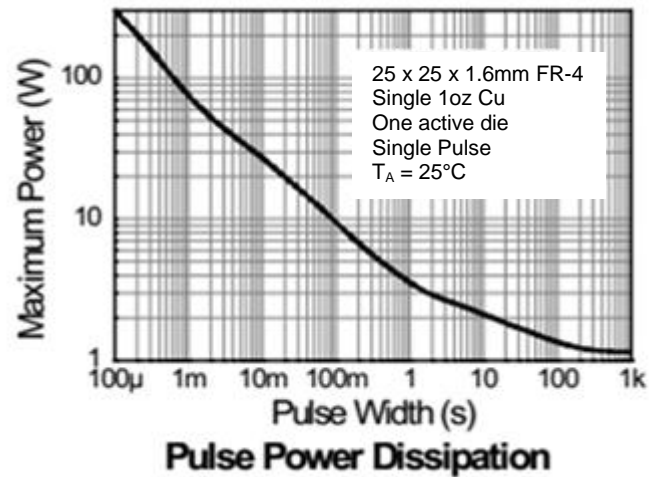
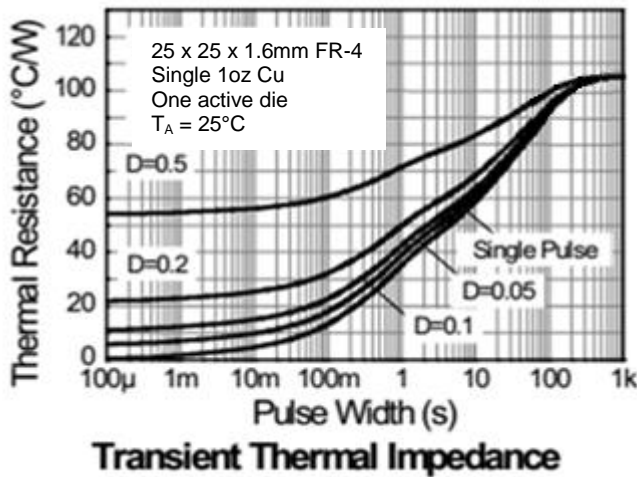
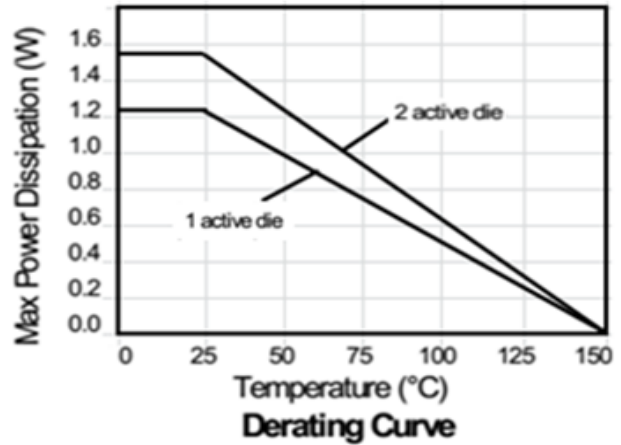
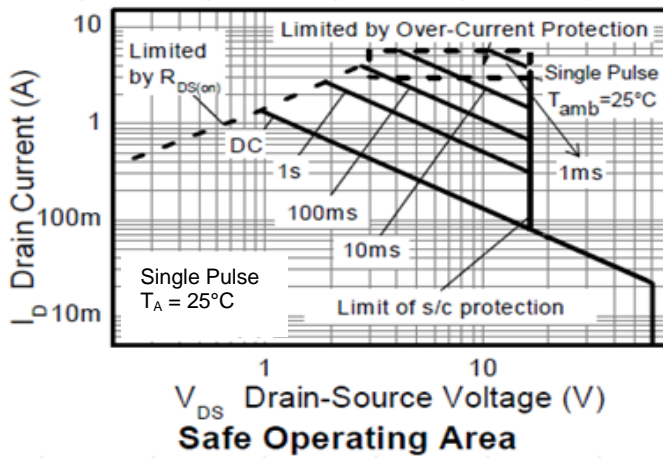
The ZXMS6005DN8Q is optimized for use with μC operating from 3.3V and 5V supplies.

| Characteristic | Symbol | Min | Max | Unit |
|---|----------|-----|------|--------------------|
| Input Voltage Range | V_{IN} | 0 | 5.5 | V |
| Ambient Temperature Range | T_A | -40 | +125 | $^{\circ}\text{C}$ |
| High Level Input Voltage for MOSFET to be On | V_{IH} | 3 | 5.5 | V |
| Low Level Input Voltage for MOSFET to be Off | V_{IL} | 0 | 0.7 | V |
| Peripheral Supply Voltage (Voltage to which load is referred) | V_P | 0 | 16 | V |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|-----------------|-------------|------------------------------|
| Power Dissipation at $T_A = +25^{\circ}\text{C}$ (Note 6) | P_D | 1.21 | W |
| Linear Derating Factor | | 9.7 | $\text{mW}/^{\circ}\text{C}$ |
| Power Dissipation at $T_A = +25^{\circ}\text{C}$ (Note 7) | P_D | 1.56 | W |
| Linear Derating Factor | | 12.5 | $\text{mW}/^{\circ}\text{C}$ |
| Thermal Resistance, Junction to Ambient (Note 6) | $R_{\theta JA}$ | 103 | $^{\circ}\text{C}/\text{W}$ |
| Thermal Resistance, Junction to Ambient (Note 7) | $R_{\theta JA}$ | 81 | $^{\circ}\text{C}/\text{W}$ |
| Thermal Resistance, Junction to Case (Note 8) | $R_{\theta JC}$ | 13.5 | $^{\circ}\text{C}/\text{W}$ |
| Operating Temperature Range | T_J | -40 to +150 | $^{\circ}\text{C}$ |
| Storage Temperature Range | T_{STG} | -55 to +150 | $^{\circ}\text{C}$ |

Notes: 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
8. Thermal resistance between junction and the mounting surfaces of drain and source pins.



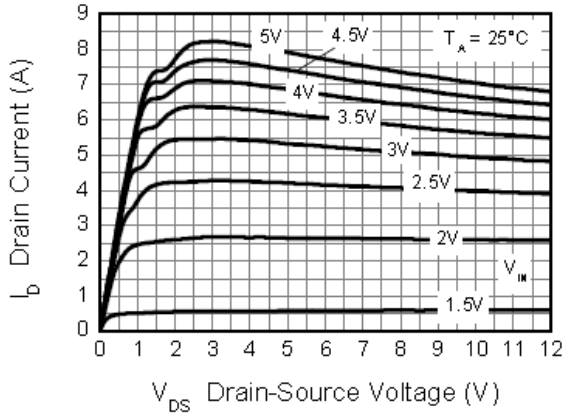
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|---|---------------------|------|------|-----|------|--|
| Static Characteristics | | | | | | |
| Drain-Source Clamp Voltage | V _{DS(AZ)} | 60 | 65 | 70 | V | I _D = 10mA |
| Off-State Drain Current | I _{DSS} | — | — | 1 | μA | V _{DS} = 12V, V _{IN} = 0V |
| | | — | — | 2 | | V _{DS} = 36V, V _{IN} = 0V |
| Input Threshold Voltage | V _{IN(TH)} | 0.7 | 1 | 1.5 | V | V _{DS} = V _{GS} , I _D = 1mA |
| Input Current | I _{IN} | — | 60 | 100 | μA | V _{IN} = 3V |
| | | — | 120 | 200 | | V _{IN} = 5V |
| Input Current while Over-Temperature Active | — | — | — | 300 | μA | V _{IN} = 5V |
| Static Drain-Source On-State Resistance | R _{DS(ON)} | — | 170 | 250 | mΩ | V _{IN} = 3V, I _D = 1A |
| | | — | 150 | 200 | | V _{IN} = 5V, I _D = 1A |
| Continuous Drain Current (Note 6) | I _D | 1.4 | — | — | A | V _{IN} = 3V; T _A = +25°C |
| | | 1.6 | — | — | | V _{IN} = 5V; T _A = +25°C |
| Continuous Drain Current (Note 7) | | 1.7 | — | — | | V _{IN} = 3V; T _A = +25°C |
| | | 1.8 | — | — | | V _{IN} = 5V; T _A = +25°C |
| Current Limit (Note 9) | I _{D(LIM)} | 2.2 | 5 | — | A | V _{IN} = 3V |
| | | 3.3 | 7 | — | | V _{IN} = 5V |
| Dynamic Characteristics | | | | | | |
| Turn On Delay Time | t _{D(ON)} | — | 6 | — | μs | V _{DD} = 12V, I _D = 0.5A, V _{GS} = 5V |
| Rise Time | t _R | — | 14 | — | μs | |
| Turn Off Delay Time | t _{D(OFF)} | — | 34 | — | μs | |
| Fall Time | t _F | — | 19 | — | μs | |
| Over-Temperature Protection | | | | | | |
| Thermal Overload Trip Temperature (Note 10) | T _{JT} | +150 | +175 | — | °C | — |
| Thermal Hysteresis (Note 10) | ΔT _{JT} | — | +10 | — | °C | — |

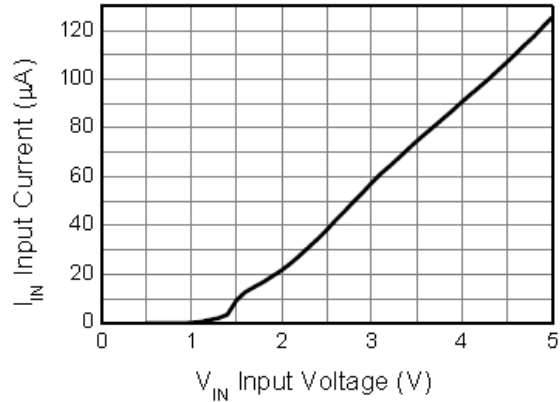
Notes: 9. The drain current is restricted only when the device is in saturation (see graph "Typical Output Characteristic"). This allows the device to be used in the onstate without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.

10. Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand over-temperature for extended periods.

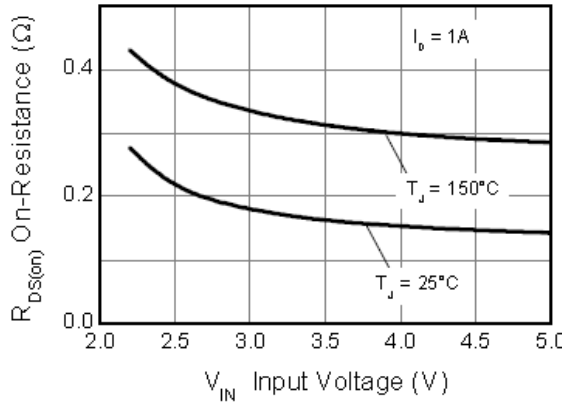
Typical Characteristics



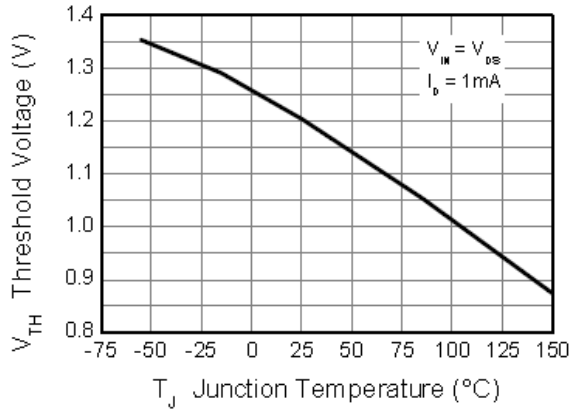
Typical Output Characteristic



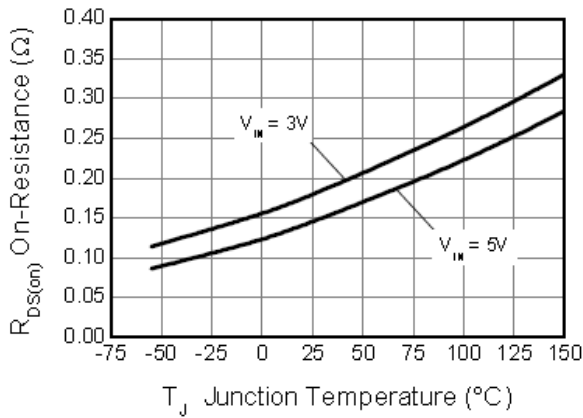
Input Current vs Input Voltage



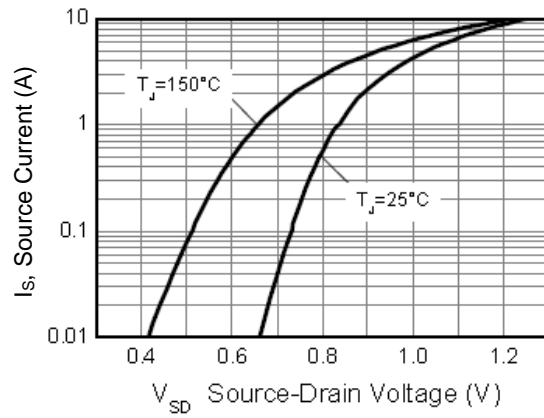
On-Resistance vs Input Voltage



Threshold Voltage vs Temperature

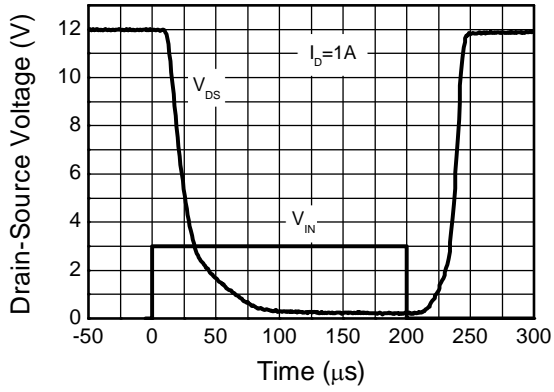


On-Resistance vs Temperature

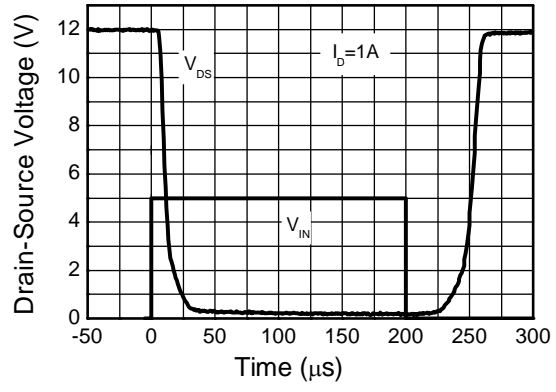


Reverse Diode Characteristic

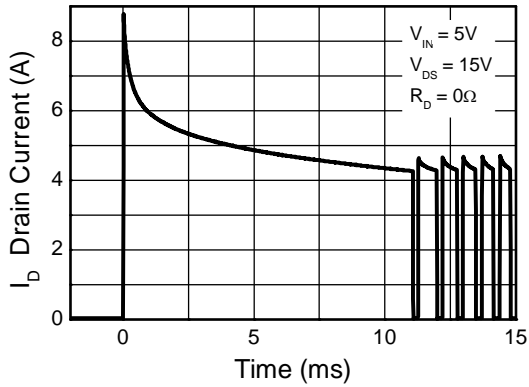
Typical Characteristics (Cont.)



Switching Speed



Switching Speed

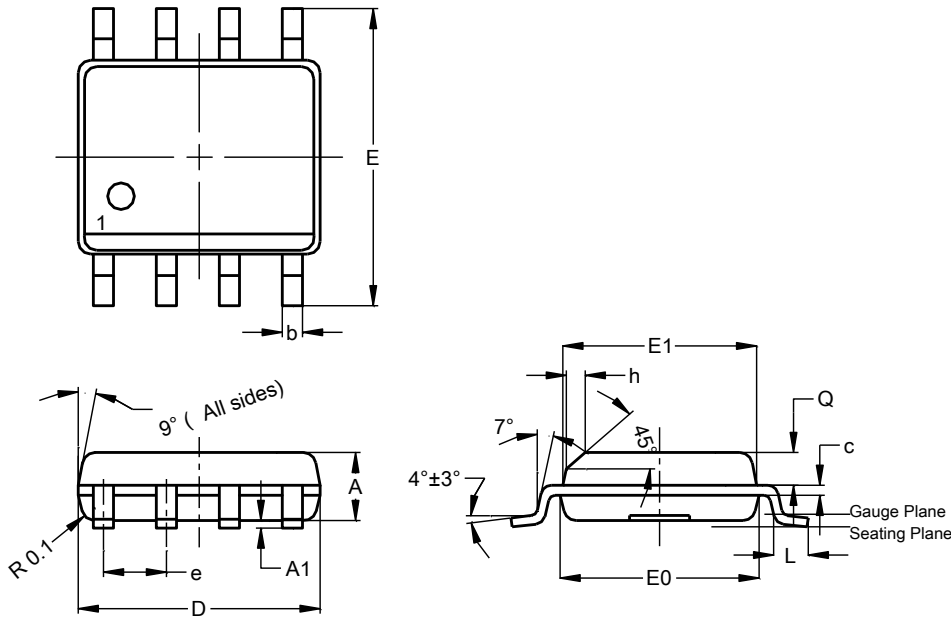


Typical Short Circuit Protection

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8

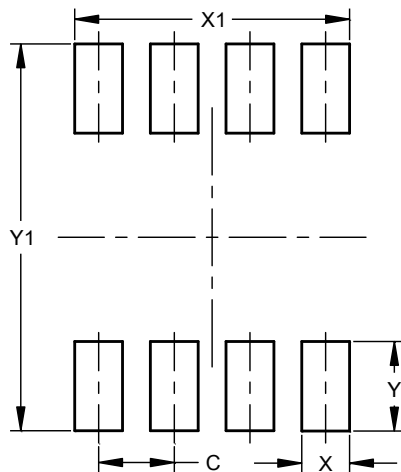


| SO-8 | | | |
|----------------------|------|------|------|
| Dim | Min | Max | Typ |
| A | 1.40 | 1.50 | 1.45 |
| A1 | 0.10 | 0.20 | 0.15 |
| b | 0.30 | 0.50 | 0.40 |
| c | 0.15 | 0.25 | 0.20 |
| D | 4.85 | 4.95 | 4.90 |
| E | 5.90 | 6.10 | 6.00 |
| E1 | 3.80 | 3.90 | 3.85 |
| E0 | 3.85 | 3.95 | 3.90 |
| e | -- | -- | 1.27 |
| h | - | -- | 0.35 |
| L | 0.62 | 0.82 | 0.72 |
| Q | 0.60 | 0.70 | 0.65 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 1.27 |
| X | 0.802 |
| X1 | 4.612 |
| Y | 1.505 |
| Y1 | 6.50 |

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